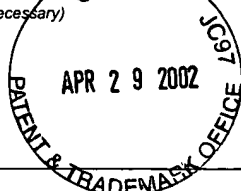


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/691004
Filing Date	October 18, 2000
First Named Inventor	Forbes, Leonard
Group Art Unit	2826
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 00303.324US4

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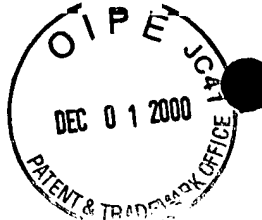
J. MOWDT

DATE CONSIDERED

May 10 '02

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached



Sheet 1 of 1

Form 1449*	Atty. Docket No.: 303.324US4	Serial No. 09/691,004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Leonard Forbes et al.	
	Filing Date: October 18, 2000	Group: Unknown

U.S. PATENT DOCUMENTS




**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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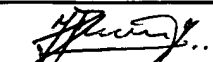
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*Substitute Disclosure Statement Form (PTO-1449)

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